



D. AMENDMENT TO THE DRAWINGS - None

E: Remarks:

The present amendment has been prepared to comply with and be in accordance with the instructions on preparing amendments issued by the U.S. Patent And Trademark Office dated 1/31/03 which waives the provisions of 37 CFR 1.121(a), (b), (c) and (d).

The above detailed amendment is believed one that should be clear on its face as to the nature of the invention sought to be recognized. The examiner is thanked for this opportunity to make clear the differences between the present application and the prior patents.

Unlike the references cited the present application is specifically directed to a *synchronous bus* interface of a computer system, where there are bits sent over multiple lines of a bus in from a sending subsystem to a receiving subsystem. A reconsideration of the rejection will show that Saito et al does not provide in a synchronous bus interface a step of "defining a delay detection and calibration phase for said synchronous communications bus system with multiple delays over multiple bus lines". Instead Saito et al delays the clock signal with a variable delay circuit that controls the delay time for his transferred standard signal for chip to chip logic transfers.

A review of claim two again shows that the claim to "an apparatus for detecting delay and adjusting all receivers in the receiving subsystem having multiple bus lines, the apparatus including worst case delay detection circuitry for each bit of the receiving subsystem bus lines ; and

control circuitry coupled to the worst case delay detection circuitry for each bit; operative to select one of two receiver paths for each bit as a function of each bit's delay detection



circuitry output and clock signals associated with each bit's receiver latch in the receiving subsystem" is not suggested by the references.

Claim 3 remains unchanged. If there is any further claim change that the examiner would suggest, a telephone call to the undersigned will be appreciated.

The review of the claims again will show, as it is respectfully submitted, that the application should be in final condition for allowance which is respectfully requested.

RESPECTFULLY SUBMITTED

(For the inventors)

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